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**1 Toward systematic design of fault-tolerant systems**

*Avizienis, A.;*

Computer, Volume: 30, Issue: 4, April 1997

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**2 Performability enhancement of fault-tolerant software**

*Tai, A.T.; Meyer, J.F.; Avizienis, A.;*

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Pages:227 - 237

[\[Abstract\]](#)   [\[PDF Full-Text \(1004KB\)\]](#)   IEEE JNL

**3 Assessment of the applicability of COTS microprocessors in high-confidence computing systems: a case study**

*Yutao He; Avizienis, A.;*

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Pages:81 - 86

[\[Abstract\]](#)   [\[PDF Full-Text \(164KB\)\]](#)   IEEE CNF

**4 A fault tolerance infrastructure for dependable computing with high-performance COTS components**

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**6 Microprocessor entomology: a taxonomy of design faults in COTS microprocessors**

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7 **Cots hardware and software in high-availability systems**

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9 **N-VERSION PROGRAMMING: A FAULT-TOLERANCE APPROACH TO RELIABILITY OF SOFTWARE OPERATION**

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10 **IN SEARCH OF EFFECTIVE DIVERSITY: A SIX-LANGUAGE STUDY OF FAULT-TOLERANT FLIGHT CONTROL SOFTWARE**

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11 **THE STAR (SELF-TESTING-AND-REPAIRING) COMPUTER: AN INVESTIGATION OF THE THEORY AND PRACTICE OF FAULT**

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15 **In search of effective diversity: a six-language study of fault-tolerant flight control software**

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